



ADTST.047AUS

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Glen Gomes et al.) Group Art Unit 2858
Serial No. : 10/618,387)
Filed : July 12, 2003)
For : EVENT PIPELINE AND SUMMING)
METHD AND APPARATUS FOR)
EVENT BASED TEST SYSTEM)
Examiner : Unknown)

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner
of Patents and Trademarks
Washington, D.C. 20231

Dear Sir:

Enclosed is a form PTO-1449 listing five references relevant to the above-identified application. A copy of each reference is also enclosed. This information disclosure statement is filed before the first office action on the merits under 37 CFR 1.97(b)(3), and thus no fee is required in accordance with 37 C.F.R. § 1.97(b)(1). The prior art references enclosed herewith are:

(1) U.S. Patent No. 5,225,772 entitled "Automatic test equipment system using pin slice architecture" and issued to Cheung et al.

(2) U.S. Patent No. 5,212,443 entitled "Event sequencer for automatic test equipment" and issued to West et al.

(3) U.S. Patent No. 5,592,659 entitled "Timing signal generator" and issued to Toyama et al.

Serial No. : 10/618,387
Filed : July 12, 2003

(4) U.S. Patent No. 6,557,133 entitled "Scaling logic for event based test system" and issued to Gomes.

(5) U.S. Patent No. 6,226,765 entitled "Event based test system data memory compression" and issued to Le et al.

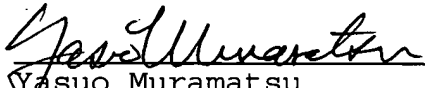
REMARKS

Applicant respectfully requests that the Examiner consider the reference disclosed in the statement.

Respectfully submitted,

MURAMATSU & ASSOCIATES

Dated: 12/29/03

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Complete if Known

(use as many sheets as necessary)

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Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U. S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.